Oocket No.: 067161-0117

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Fre Application of

Customer Number: 20277

Shinji TANAKA

Confirmation Number: 8196

Application No.: 10/682,153

Group Art Unit: 2824

Filed: October 10, 2003

Examiner: Hien N. Nguyen

For: SEMICONDUCTOR MEMORY DEVICE INPUTTING/OUTPUTTING DATA AND PARITY DATA IN

BURST OPERATION

INFORMATION DISCLOSURE STATEMENT

Mail Stop IDS Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

This Information Disclosure Statement is being filed under Rule 37 CFR 1.97(i), wherein applicants are submitting references before the grant of a patent to be placed in the file.

Accordingly, copies of the references as listed on the attached Form PTO-1449 are submitted herewith for placement in the file. No certification or fees are deemed necessary.

Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

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Facsimile: 202.756.8087 **Date: July 15, 2005**

Please recognize our Customer No. 20277 as our correspondence address.

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			\mathbf{U}	.S. PATEN	T DOCUMENTS						
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^{*}EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.